



PTO-1449 Information Disclosure Citation in an Application	Application No. 10/821,034	Applicant(s) Jiang, et al.		
	Docket Number 073338.0178	Group Art Unit 2819	Filing Date April 8, 2004	

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
Q	A	5,266,952	11/30/93	Stone, et al.	341	156	03/30/1992
Q	B	6,313,780	11/06/01	Hughes, et al.	341	156	09/27/1999
Q	C	2003/0080894 A1	05/01/03	Konno	341	162	10/25/2001
Q	D	2002/0175843 A1	11/28/02	Sonkusale, et al.	341	120	05/23/2002
	E						
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	G						
	H						
	I						
	J						
	K						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
Q	L	WO 2004/010586	07/08/03	PCT	H03M	1/00	x	
	M							
	N							
	O							

NON-PATENT DOCUMENTS

	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
Q	P European Search Report mailed May 25, 2005 re EP Application No. 05 25 0673 (3 pages)	05/25/2005
Q	Bracey, et al., "A 70 MS/s 8-Bit Differential Switched-Current CMOS A/D Converter Using Parallel Interleaved Pipelines" pp. 143-146, © 1995 IEEE. "NO MENTH"	05/1995
	S	

EXAMINER

DATE CONSIDERED

11/10/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.